

PATENT Docket No. 204552016500

CERTIFICATE OF HAND DELIVERY

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D.C. on March 12, 2003.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of:

Yuichi SATO

Serial No.:

09/412,328

Filing Date:

October 5, 1999

For:

STATIC RANDOM ACCESS MEMORY AND SEMICONDUCTOR DEVICE USING MOS TRANSISTORS HAVING CHANNEL REGION ELECTRICALLY

CONNECTED WITH GATE

Examiner: D. A. Wille

Group Art Unit: 2814

REQUEST FOR A CORRECTED FILING RECEIPT

Commissioner for Patents Washington, D.C. 20231

Sir:

Please furnish the undersigned with a corrected Filing Receipt for the above-identified application. The corrections needed are as follows:

The applicant's city of residence should be **Naga-gun**.

A copy of the marked-up Filing Receipt is attached.

In the unlikely event that the transmittal letter is separated from this request and the Patent Office determines that a fee is required, applicant petitions for any required relief

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including extensions of time and authorizes the Commissioner to charge the cost of such petitions and/or other fees due in connection with the filing of this document to **Deposit Account**No. 03-1952 referencing 204552016500.

Respectfully submitted,

Dated: March 12, 2003

By:

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DRAWINGS APPLICATION NUMBER FILING DATE GRP ART UNIT FIL FEE REC'D ATTY.DOCKET.NO TOT CLAIMS IND CLAIMS 2814 760

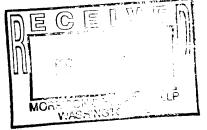
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CONFIRMATION NO. 9541 CORRECTED FILING RECEIPT

Date Mailed: 02/21/2003

Receipt is acknowledged of a CPA in this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

YUICHI SATO, MIEREN, JAPAN:

Domestic Priority data as claimed by applicant

Foreign Applications

JAPAN 10-282335 10/05/1998

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Non-Publication Request: No

Early Publication Request: No

Title

STATIC RANDOM ACCESS MEMORY AND SEMICONDUCTOR DEVICE USING MOS TRANSISTORS HAVING CHANNEL REGION ELECTRICALLY CONNECTED WITH GATE

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